



07-14-05

EFN

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | | |
|----------------------|---|----------------|-----------|
| Application Number | 10/771678 | Docket Number | CNTR.2094 |
| Filed | 2/4/2004 | Group Art Unit | |
| Examiner | | Customer No. | 23669 |
| Application Title | PIPELINED MICROPROCESSOR, APPARATUS, AND METHOD FOR GENERATING EARLY STATUS FLAGS | | |
| First Named Inventor | GERARD M. COL | | |

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims, and return an initialed copy of each form to the undersigned practitioner.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 CFR 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b) or is available as a reference under 35 U.S.C. §102 *et seq.* Applicant reserves the right to swear behind or otherwise disprove the allege "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 CFR 1.56, 1.97, and 1.98, and the Manual of Patent Examining Procedures §609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

Some of the enclosed documents may have markings thereon. No significance is intended to be attached to the markings.

EXPRESS MAIL LABEL NUMBER: EO 005 289 215 US
DATE OF DEPOSIT: 7/13/05

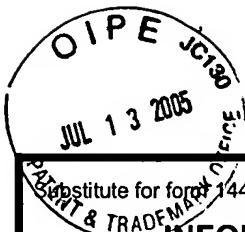
I hereby certify that this paper is being deposited with the U.S. Postal Service Express Mail Post Office to Addressee Service under 37 C.F.R. §1.10 on the date shown above and is addressed to Mail Stop AMENDMENT, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450. *Tanya Locke*

Respectfully submitted,

By *C. Alan Davis*

E. ALAN DAVIS
Reg. No. 39,954
Tel.: (512) 301-7234

Date *7-12-05*



| | | | | | |
|--|---|----|---|--------------------------|---------------|
| U.S. Patent and Trademark Office Information Disclosure Statement by Applicant (use as many sheets as necessary) | | | | Complete if Known | |
| Sheet | 1 | of | 1 | Application Number | 10/771678 |
| | | | | Filing Date | 2/4/2004 |
| | | | | First Named Inventor | GERARD M. COL |
| | | | | Group Art Unit | |
| | | | | Examiner Name | |
| | | | | Attorney Docket Number | CNTR.2094 |

| U.S. PATENT DOCUMENTS | | | | | |
|------------------------------|-----------------------|---|--------------------------------|--|---|
| Examiner Initials* | Cite No. ¹ | Document Number | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
| | | Number-Kind Code ^{2(if known)} | | | |
| | | 6079014 | 06-20-2000 | Papworth et al. | |
| | | 6219778 | 04-17-2001 | Panwar et al. | |
| | | 5043868 | 08-27-1991 | Kitamura et al. | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |

| NON PATENT LITERATURE DOCUMENTS | | | | |
|--|-----------------------|---|--|----------------|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | | T ² |
| | AA | Jim Handy, The Cache Memory Book, 2nd Edition, 1998, pages 47 and 222. | | |
| | AB | FARCY A ET AL: "Dataflow analysis of branch mispredictions and its application to early resolution of branch outcomes" MICRO-31. PROCEEDINGS OF THE 31ST. ANNUAL ACM/IEEE INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE. DALLAS, TX, NOV. 30 - DEC. 2, 1998, PROCEEDINGS OF THE ANNUAL ACM/IEEE INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE, LOS ALMITOS, CA : IEEE COMP. SOC, US, 30 November 1998 (1998-11-30), pages 59-68, XP010318259 ISBN: 0-8186-8609-X * page 64, paragraph 3 - page 66, paragraph 3.3* | | |
| | | | | |
| | | | | |

| | |
|--------------------|-----------------|
| Examiner Signature | Date Considered |
|--------------------|-----------------|

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

If you need assistance in completing this form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.